## **FE Review**

#### **Computer Terms**

2/2/2011

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## Computers sizes

#### Supercomputers

- Mainframe centralized, large memory, large peripherals
- Mini-computers Less memory, smaller
- Microcomputers smaller still few peripherals, slower than all, can be used as standalone devices

## Architecture

- Input
- Output
- Memory
- · CPU

## The Microprocessor

- $\cdot$  CPU on a single chip
  - » Control Unit
  - » Logic Unit

- » Accumulators
- » Specialized and non-specialized registers

# Special registers

- Accumulators: Hold data and instructions while they are being worked on and for future use. The accumulator is a special case register. More is better!
- **Program Counter (PC):** Special register which holds the address of the NEXT instruction to be executed

# Special Registers (continued)

**Stacks:** temporary storage of data or address in sequential order

- FIFO => First in First Out
- LIFO =>Last in First Out
- Sometimes called a "Push-down register"
- Flag Register: Holds individual indicators that specific actions have taken place (usually within the accumulators)
- Instruction Register (IR): holds current instruction being executed.



# Base 2 (binary system)

Computers use a number system known as binary which is a base 2 system. Since it is a base 2 system, the highest number in the system is a 1.

$$\frac{1}{2^{4}} \begin{array}{c} 0 \ 0 \ \frac{1}{2^{1}} \\ 2^{1} \end{array} \begin{array}{c} \frac{1}{2^{0}} \\ \frac{1}{2^{0}} \end{array}$$

$$\left(1 \times 2^{4}\right) + \left(1 \times 2^{1}\right) + \left(1 \times 2^{0}\right)$$

$$16 + 2 + 1 = 19$$

## Base 10 to Base 2 Conversion

Con	vert	42 10	to	Binar	<b>ч (В</b>	ase	2)		
powers of need remainde	two ⇒ 42 er ⇒	32 1 42	16 <b>O</b> 10	8 1 10	4 0 2	2{ <b>1</b> }2	Į	1 0 2 0	
		:	= 42	10					
bit	8	7	6	5	4	3	2	1	0
#	273	17	17	17	17	1	1	1	1
power	256	128	64	32	16	8	4	2	1
273	1	0	0	0	1	0	0	0	12

# Base 8 (octal system)

Conv	ert	175	50 <sub>8</sub>	to be	<b>ase</b> 10	
1	*	<b>8</b> <sup>3</sup>	=	1*!	512	512
7	*	<b>8</b> <sup>2</sup>	=	7 *	64	448
5	*	<b>8</b> <sup>1</sup>	=	5 *	8	40
0	*	<b>8</b> °	=	0 *	1	0
						1,000



## Base 10 to Octal conversion

# Binary to Octal Conversion

Convert 10110111<sub>2</sub> to Octal Start from the radix point and group into groups of three. Feel free to pad the left most group to make a group of three.  $\underbrace{\begin{array}{c}0 & 1 & 0\\2 & 6\end{array}}_{7} \underbrace{\begin{array}{c}1 & 1 & 1\\2\end{array}}_{7} \xrightarrow{} 267_{8}$ 

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# Octal to Binary Conversion

# Convert 52, to Binary $5_{101}$ $2_{8}$ $\Rightarrow$ 101010<sub>2</sub>

# Base 16 (hex system)

Co	nve	rt Al	)85	16 <b>to b</b>	ase <sub>10</sub>	
A	*	<b>16</b> <sup>3</sup>	=	10 * 4	l, 096	40,960
D	*	16 <sup>2</sup>	=	13 *	256	3 328

\*  $16^2 = 13$  \* 256 3,328 \*  $16^1 = 8$  \* 16 128 \*  $16^0 = 5$  \* 1 5

**44, 421**<sub>10</sub>

8

Base 10 to Base 16 Conversion
Convert 2748 to Hex.
As with the other systems, we
will use the "Sum of Weights" method.
256 16 1
A B C
2748 188 12
So the answer is $ABC_{16} \Leftarrow$
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## Base 16 to Binary Conversion

# Convert $5AC_{16} \Rightarrow Base 2$ $5 A C_{16} \Rightarrow 1011010100_{2}$

## Data Sizes

The smallest changeable unit of data is known as the BIT.

The letters stand for Binary digiT.

### $1 \ 0 \ 0 \ 1_2 = 9_{10} \Rightarrow 4 \text{ bits} = 1 \text{ nibble}$

The next data size up is the Nibble which is 4 bits long.

## Data Sizes (continued)

- $\cdot$  Byte = 8 Bits
- Word = 16 Bits = 2 Bytes
- Long Word = 32 Bits = 4 bytes

Double Word = 64 Bits = 4 Words

# Data Types

#### Character data - ASCII

• American Standard Code for Information Interchange

#### Numerical data – Binary

- Commonly used by mini- and micro- computers
- Allows a maximum of 128 different characters.

#### EBCDIC

- Extended Binary Coded Decimal Interchange Code
- Used extensively by mainframes
- Allows a maximum of 256 characters

# Parity System

This is an extremely simple way of making sure that single errors in data interchange does not occur. There are two basic types of parity: Even or Odd.

A bit position is reserved somewhere in the data Word (or larger data structure). Then the number of 1's in the word are counted. If Even Parity is desired, then the total number of ones Needs to be EVEN. If it isn't, then the Parity bit is set to a 1 which makes the total even. Otherwise, it is set to a 0. The same process applies to Odd parity.

## Memory Size

Memory size uses prefixes such as K, M, and G

1k x 8 of ram is  $2^{10} \times 8$  bits wide of data.  $2^{10} \times 8 = 1,024 * 8 = 8,192$  bits. Note that The K here did not mean 1000 exactly. Nor Will a Gig (G) actually mean  $10^9$  exactly.

# Memory Types

- Volatile memory (Lost when power is lost)
  - Random Access Memory (RAM)
- Non-volatile memory
  - Read Only Memory (ROM)
  - Programmable ROM (PROM)
  - Erasable PROM (EPROM)
  - Hard Drives, CD/DVD ROMS, Floppies, etc.

## **Example Question**

Which of the following types of memory is lost when a computer's power is interrupted?

<b>(</b> A <b>)</b>	RAM
<b>(</b> B <b>)</b>	ROM
(C)	PROM
(D)	EPROM

## **Bus'**

- Address Bus Carries address locations
- Control Bus Transfers control and and status information
- Data Bus Carries the actual data
   Series or parallel

# **Example Question**

A 256k-word memory uses 16 bit-words. How many parallel data lines are required to pass data to the CPU for processing?

(A)	2
<b>(B)</b>	8
(C)	9
(D)	16

The key word here is "Parallel". All bits in a word are passed in parallel to the CPU. There is one data line per bit in the word.

## **Command Sets**

- Complex Instruction Set (CISC)
- Reduced Instruction Set (RISC)

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- Used to increase speed. Fewer, but more powerful instructions.

# **Operating System**

Sometimes referred to as the OS, this manages everything that the computer does with regards to memory, processor operation scheduling, access to/form peripherals, I/O, resolves any conflicts with resources. The OS is often also referred to as the BIOS or "Basic input/output System".

# Multi-Tasking

Main memory is allocated between several users with different applications running at the same time. It is also known as Multi-Programming. If the term USER is literally different people, then it can also be called a Multi-User system.

# Time Sharing

Also known as swapping, (not that type of swapping!!. Lets keep this out of the gutter, OK?!!). This is a technique where each user takes a turn using the OS for a specific period of time, (less than one second). At the end of the users time, the active memory is stored in a private area (assigned to the user), and then the next users private memory area is loaded into active memory.

## Run Modes

Batch Mode

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Real-time mode (also known as interactive mode.

## Structured Programming



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## **Example Question**



The variable t in the flowchart below has an initial value of 0.5. What is the value of the variable p at the conclusion of the routine?

<b>(A)</b>	2.9
<b>(B)</b>	3.334
(C)	4.0
(D)	4.44

teration	n	P	b	b  < †?
1	1	4.00	<b>-4</b> /3	no
2	2	2.67	4⁄5	no
3	3	3.47	-4/7	no
4	4	2.9	4/9	yes

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## **Example Question**



# Algorithms

#### Language independent



AAAAA	IF-THE	N-single statement
	Algorithm	IF – <u>condition</u> – THEN <u>single statement</u> END IF
	<b>Fortran</b> 2/2/2011	<b>IF</b> (condition) statement

IF	-THEN-multiple statements	
Algorithm	IF – <u>condition</u> – THEN <u>Multiple statements</u> END IF	
Fortran	IF ( <u>not - condition</u> ) GO TO 10 <u>statements</u> 10 CONTINUE	)
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# IF-THEN-ELSE

		IF - <u>condition</u> - THEN
		<u>statements</u>
Algorithm		ELSE
		<u>statements</u>
		END IF
	IF	(not - condition) GO TO 5
		<u>statements</u>
Fontnon		GO TO 6
Fortran	5	CONTINUE
		<u>statements</u>
	6	CONTINUE

	DO WHILE
Algorithm	DO WHILE – <u>condition</u> <u>statements</u> END DO
Fortran	<ul> <li>5 IF (not - condition) GO TO 6 <ul> <li>statements</li> <li>GO TO 5</li> <li>6 CONTINUE</li> </ul> </li> </ul>
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	DO UNTIL
Algorithm	DO UNTIL – <u>condition</u> <u>statements</u> END DO
Fortran	5 CONTINUE <u>statements</u> IF (not - condition) GO TO 5
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A A A A A A A A A A A A A A A A A A A	DO FOR	
	DO FOR $I \leftarrow L$ TO M BY N	
	Algorithm <u>statements</u>	
	END DO	
	$DO \ 10 \ I = L, M, N$	-
	Fortran <u>statements</u>	
	10 CONTINUE	
	(, N can be omitted in which case	
	, 1 is assumed)	43



# Multi-way selection ELSE-IF Algorithm

- IF condition(1) THEN
  - statements
- ELSE IF condition(2) THEN
  - statements
- ELSE IF condition(3) THEN
  - statements
- ELSE condition(4)
  - statements

END IF

## Multi-way selection ELSE-IF Fortran

- IF (not condition(1)) GO TO 10 statements
- GO TO 20
- 10 IF (not condition(2)) GO TO 11 statements

GO TO 20

 $\begin{array}{c} 11 \quad \text{IF} \quad \left( \underbrace{\text{not} - \text{condition(3)}} \\ \underline{\text{statements}} \end{array} \right) \quad \textbf{GO TO 12} \\ \end{array}$ 

GO TO 20

- **12 CONTINUE** 
  - statements

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20 CONTINUE

## Multi-way selection CASE Algorithm

CASE variable OF

1:

statements

2:

statements

3:

statements

ELSE

statements

END CASE

ELSE is optional

## Multi-way selection CASE Fortran

IF (var iable .LT. 1) GO TO 20 IF (var iable .GT. 3) GO TO 20 GO TO (11,12,13), <u>var iable</u> **11 CONTINUE** statements **GO TO 30 12 CONTINUE** statements GO TO 30 **13** CONTINUE statements GO TO 30 **30** CONTINUE





